

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
Tomoharu TANAKA et al.)	Group Art Unit: 2824 (parent)
)	
Serial No.: Rule 53(b) Continuation Application of)	Examiner: Son T. Dinh (parent)
Serial No. 10/315,030)	
)	Attorney Docket No. 001701.00193
Filed: herewith)	
)	
For: SEMICONDUCTOR MEMORY DEVICE)	

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with Applicants' duty of disclosure, the following information is submitted for consideration by the United States Patent and Trademark Office in connection with the above-captioned application. The information is identified on the attached PTO/SB/08A form.

This application relies, under 35 U.S.C. § 120, on the earlier filing date of prior U.S. patent application Serial No. 10/315,030 (filed December 10, 2002), which relied in turn on the earlier filing date of prior U.S. patent application Serial No. 10/024,189 (filed December 21, 2001), which relies in turn on the earlier filing date of prior U.S. patent application Serial No. 09/768,588 (filed January 25, 2001), which relies in turn on the earlier filing date of prior U.S. patent application Serial No. 09/504,903 (filed February 16, 2000, now U.S. Pat. No. 6,208,573), and which relies in turn on the earlier filing date of prior U.S. patent application Serial No. 09/055,216 (filed April 6, 1998, now U.S. Pat. No. 6,064,611). The references identified on the attached PTO/SB/08A form were submitted to and/or cited by the USPTO in one or more of these prior applications. Pursuant to 37 C.F.R. §1.98(d), copies of these references are not

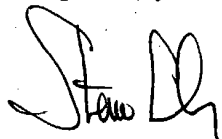
included with this paper. Applicants would be happy to supply additional copies of these references upon request.

Applicants do not waive any rights to take appropriate action to establish patentability over the listed documents should they be applied as references against the claims of the present application. Applicants also wish to bring to the attention of the Examiner commonly assigned, Patent Nos. 6,028,792 and 5,847,992, which are related to this application by subject matter.

It is respectfully requested that the Examiner fully consider each of the documents, initial the enclosed PTO/SB/08A form in the appropriate place to indicate that the document has been considered, and return a copy of the initialed form to the undersigned in accordance with MPEP Section 609.

The present paper is being filed concurrently with the present application, and Applicants believe that no fee is necessary pursuant to 37 C.F.R. § 1.97(b)(1). However, if a fee is due, the Commissioner is authorized to charge Deposit Account No. 19-0733.

Respectfully submitted,

 #42,402

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Dated: July 21, 2003

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
				Application Number	TBA; Continuation of SN 10/315,030
				Filing Date	herewith
				First Named Inventor	Tomoharu Tanaka
				Group Art Unit	2824 (parent)
				Examiner Name	Son T. Dinh (parent)
				Attorney Docket Number	001701.00193
Sheet	1	of	2		

U.S. PATENT DOCUMENTS					
Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
		US-5,299,162	03-29-1994	Kim et al.	
		US-5,345,418	09-06-1994	Challa	
		US-5,539,690	07-23-1996	Talreja et al.	
		US-5,570,315	10-29-1996	Tanaka et al.	
		US-5,596,526	01-21-1997	Assar et al.	
		US-5,751,637	05-1998	Chen et al.	
		US-5,757,699	05-26-1998	Takeshima et al.	
		US-5,826,600	10-1998	Rowe et al.	
		US-5,847,992	12-08-1998	Tanaka et al.	
		US-5,892,710	04-06-1999	Fazio et al.	
		US-5,892,722	04-06-1999	Jang et al.	
		US-5,896,328	04-20-1999	Tanizaki et al.	
		US-5,917,767	06-29-1999	Ohta	
		US-6,028,792	02-22-2000	Tanaka et al.	
		US-6,064,611	05-16-2000	Tanaka et al.	
		US-6,163,048	12-19-2000	Hirose et al.	
		US-6,208,573B1	03-27-2001	Tanaka et al.	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
		JP 08-279297	10-22-1996	Samsung Electron Co Ltd.		Abstr.
		JP 07-230695	08-29-1995	Toshiba Corp.		Abstr.

Examiner Signature		Date Considered	
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***EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04.

³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 2

2

Complete if Known

Application Number	TBA; Continuation of SN 10/315,030
Filing Date	herewith
First Named Inventor	Tomoharu Tanaka
Group Art Unit	2824 (parent)
Examiner Name	Son T. Dinh (parent)
Attorney Docket Number	001701.00193

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Tae-Sung JUNG et al.; "A3.3V 128 Mb Multi-Level NAND Flash Memory for Mass Storage Applications," ISSCC Digest of Technical papers, pp. 32-33, Feb. 1996	
		Tae-Sung JUNG et al.; "A 117-mm2 3.3-V 128-Mb Multilevel NAND Flash Memory For Mass Storage Applications," IEEE Journal of Solid-State Circuits, Vol. 31, No. 11, November 1996, pp. 1575-1583	
		Young, Joon Choi et al.; "A High Speed Programming Scheme For Multi-Level NAND Flash Memory," Samsung Electronics Co., Ltd., Kiheung, Korea, 1996 Symposium on VLSI Circuits Digest of Technical Papers, pp. 170-171	

Examiner
SignatureDate
Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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